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**AUG 24 2006**

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## FACSIMILE COVER SHEET

To: Commissioner for Patents for Examiner Carlos D. Amaya Group Art Unit 2836	Facsimile No. 571/273-8300
From: Stephanie Fay Legal Assistant to Betty Formby	No. of Pages Including Cover Sheet: 3
<p>Enclosed herewith:</p> <ul style="list-style-type: none"><li>• Applicant Initiated Interview Request Form (PTOL-413A); and</li><li>• Agenda for Telephone Interview.</li></ul>	
Re: Application Serial No. 09/938,081 Attorney Docket No. 01-840	
Date: August 24, 2006	
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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Jablonski et al.

Serial No.: 09/938,081

Filed: August 23, 2001

For: Interface Circuit for Providing  
A Computer Logic Circuit with First  
and Second Voltages and an  
Associated Method

§ Group Art Unit: 2836

§

§ Examiner: Amaya, Carlos David

§

§ Attorney Docket No.: 01-840

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AGENDA FOR TELEPHONE INTERVIEW

Sir:

For our telephone interview scheduled on August 25, 2006, please consider the following claim amendments for discussion:

1. (Currently amended) An interface circuit for supplying a computer logic circuit with first and second inputs having respective first and second predetermined voltage levels that are different from each other and from ground, respectively, the interface circuit comprising:

a first power supply circuit for providing the first input having the first predetermined voltage level in response to a first supply voltage;

a regulator for generating an output having the second predetermined voltage level in response to the first supply voltage; and

a second power supply circuit for providing an output having the second predetermined voltage level in response to a second supply voltage that is different from the first supply voltage and from ground,

wherein said regulator and said second power supply circuit cooperate to provide the second input having the second predetermined voltage level in instances in which the second supply voltage is present, [[, and]]

~~wherein said regulator supplies provides the second input having the second predetermined voltage level in a manner independent of said second power supply circuit in instances in which the second supply voltage is unavailable.~~

Both **Alston** and **Halim** provide a single voltage to the logic circuit, as shown in the figures on the next page.

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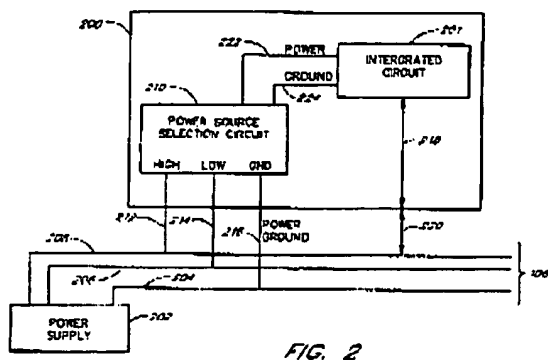
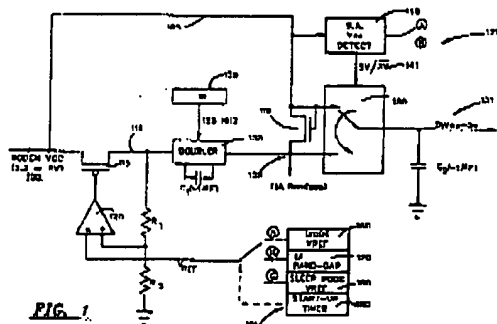


FIG. 2

Alston

Figure 2 of Alston shows power supply 220, integrated circuit 201, and power source selection circuit 210 providing an interface between the power source and integrated circuit. The power source selection circuit receives two inputs plus ground, but only sends a single power line plus ground to the integrated circuit.



Halim

On the left side of the figure 1 of Halim, "[m]odem VCC 100 for the IA can be either 3.3 volt or 5 volt as supplied by a system such as a notebook computer" (Halim, column 3, lines 20-21). On the right side of the figure, the single output is labeled "IA Vcc=5v". Clearly there is only a single input and a single output in this circuit.

The Examiner is invited to call at the below-listed telephone number to confirm or reschedule the requested telephone interview.

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